ECE 477 LAB 11

1) By using the solution of Q3 in lab work 10 upload the modified counter implementation to the Genesys board.

2) Modify the question 1 to 0-255 counter. Do related simulations.

3) Define an input that controls the counter whether counts up or down. Modify the question 2.

4) A new type of clock divider will be introduced.

Homework

   1) 2's complements circuit
   2) 8-Bit Magnitude Comparator
   3) 16-Bit Magnitude Comparator
   4) 32-Bit Magnitude Comparator
   5) 1's complements circuit (by using XOR gates)